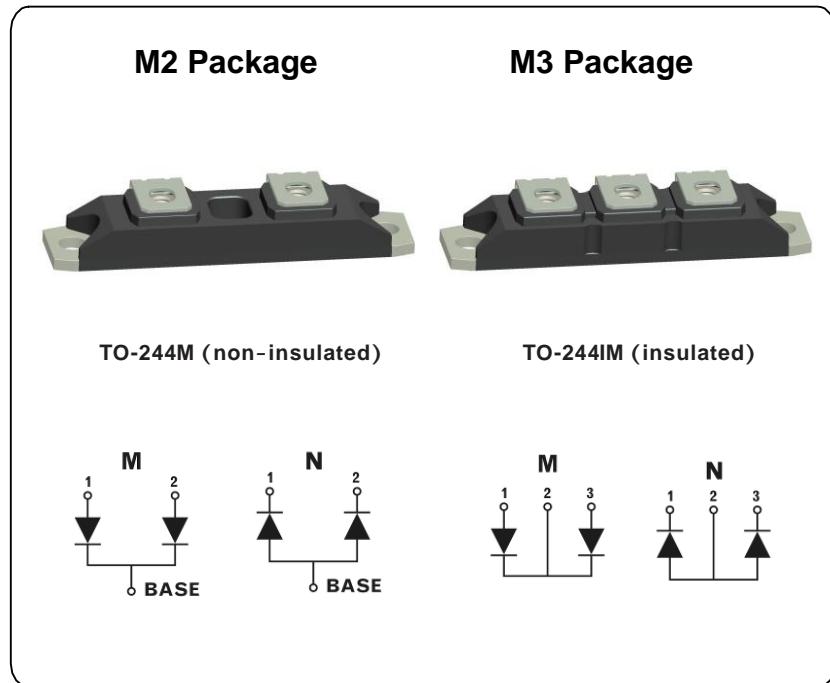


CE High Power Products
Super Fast Recovery Diode ,200A
Features

- International standard package with DCB ceramic base plate
- Dual Diode construction
- Low Leakage Current
- Low forward voltage drop
- High surge current capability
- Super Fast Switching

Typical Applications

- Antiparallel diode for high frequency switching devices
- Free wheeling diode in converters and motor control circuits
- Inductive heating and melting
- Uninterruptible power supplies (UPS)
- Ultrasonic cleaners and welders


Maximum Ratings

Parameter	Symbol	Test Conditions	Values	Unit
Repetitive peak reverse voltage	V_{RRM}		1200	V
RMS reverse voltage	V_{RMS}		840	V
DC blocking voltage	V_{DC}		1200	V
Average forward current	$I_{F(AV)}$	$TC \leq 120^{\circ}C$	200	A
Non-repetitive forward surge current,half sine-wave	I_{FSM}	$TC = 25^{\circ}C$	1400	A

Electrical Specifications

Parameter	Symbol	Test Conditions	Values	Unit
DC forward voltage	V_F	$I_F = 100A, T_J = 25^{\circ}C$	1.9(typ)	V
Maximum DC reverse current	I_R	$V_R = \text{Rated } V_{RRM}, T_J = 25^{\circ}C$	10	uA
		$V_R = \text{Rated } V_{RRM}, T_J = 125^{\circ}C$	200(typ)	uA
Maximum Reverse Recovery Time	t_{rr}	$I_F = 0.5A, I_R = 1A, I_{RR} = 0.25A$	120(typ)	ns
Reverse recovery charge(Area Under the curve Defined by IRRM And trr)	Q_{rr}	$V_{DD} = 100V, I_F = 1A, dI/dt = 200A/\mu s;$	310(typ)	nc
Diode Peak Reverse Recovery Current	I_{RRM}		7.0(typ)	A
$S = tb/ta$	S		0.6	

Thermal - Mechanical Specifications

Parameter	Symbol	Test Conditions	Values	Unit
Thermal resistance junction to case(M2/M3)	R_{thjc}	Per diode	0.14/0.35	°C/W
Junction and storage temperature range	T_J, T_{stg}		-40 to 150	°C
Mounting Torque	M_t	To terminals(M6)	$4 \pm 15\%$	Nm
	M_s	To heatsink(M6)	$5 \pm 15\%$	
Module(Aproximately)	Weight		85	g
Maximum RMS insulation voltage (for insulated type)	M3 (Insulated)	Visol	Ac.50Hz; R.M.S; 1min	2500
			Ac.50Hz; R.M.S; 1sec	3500

Performance Curves

Fig.1 Forward Current vs Forward Voltage

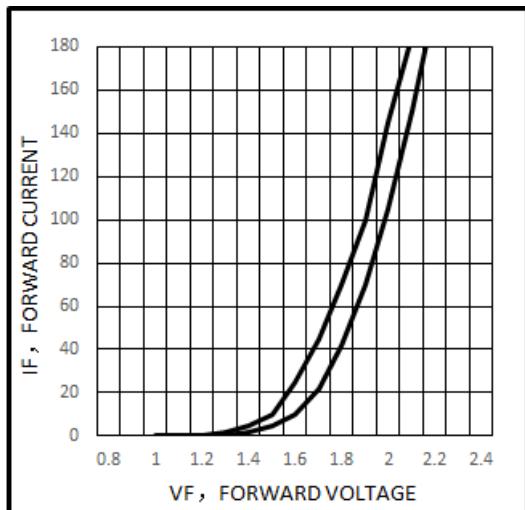


Fig.2 Reverse Current vs Reverse Voltage

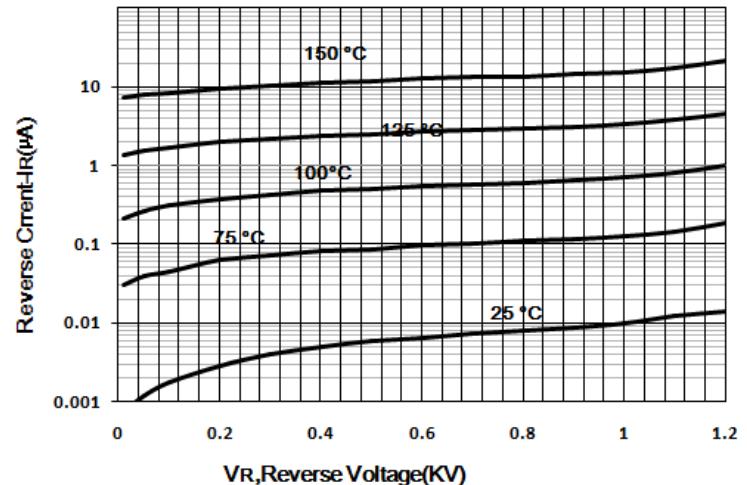


Fig.3 Reverse Recovery Parameter Test Circuit

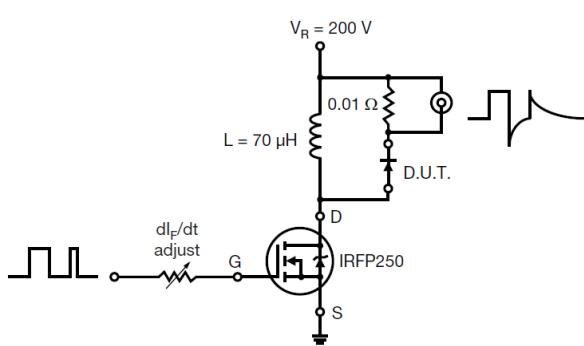
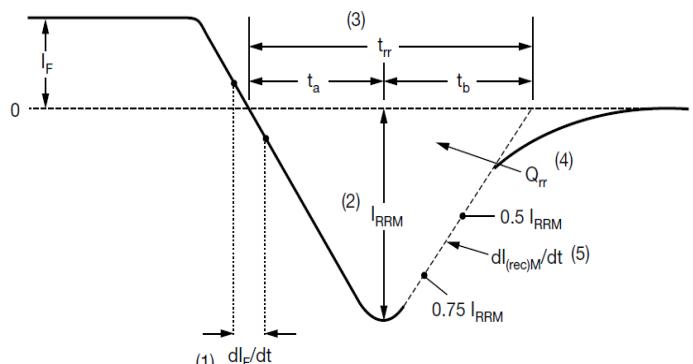


Fig.4 Reverse Recovery Waveform and Definitions



(1) dl_F/dt - rate of change of current through zero crossing

(4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

(2) I_{RRM} - peak reverse recovery current

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through $0.75 I_{RRM}$ and $0.50 I_{RRM}$ extrapolated to zero current.

(5) $dl_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Device code**C M DU 200 I M - 12**

CE's power module

"M" = common Cathode configuration

"N" = common Anode configuration

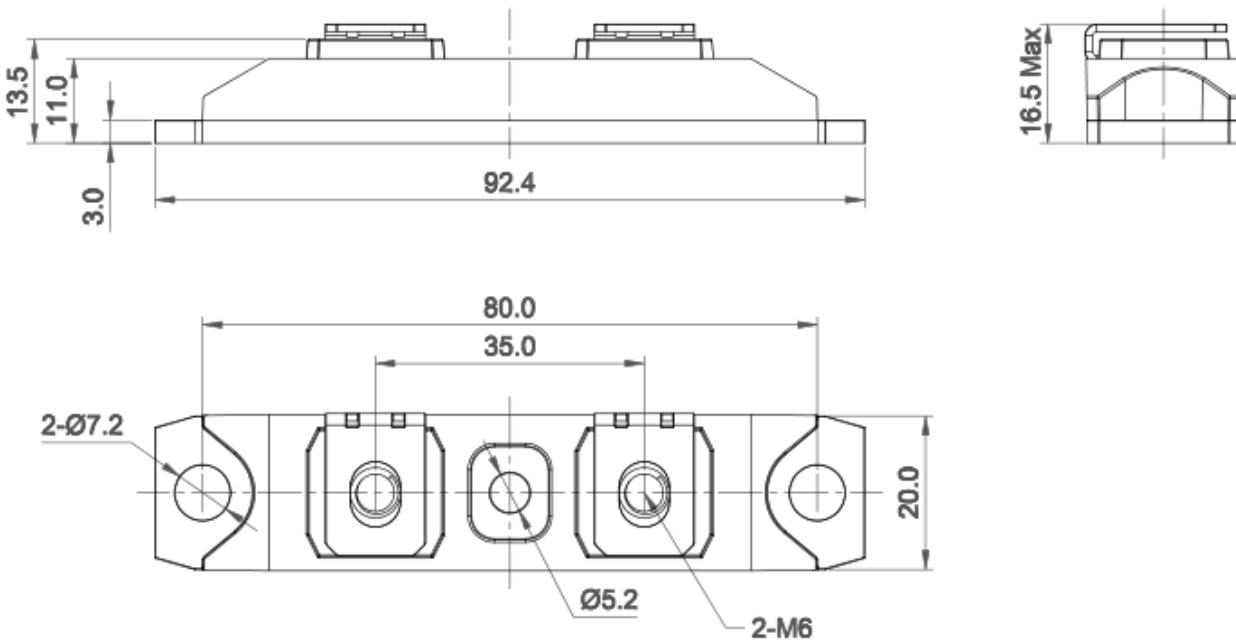
DS for Schottky Barrier Diode

Maximum average forward current, A

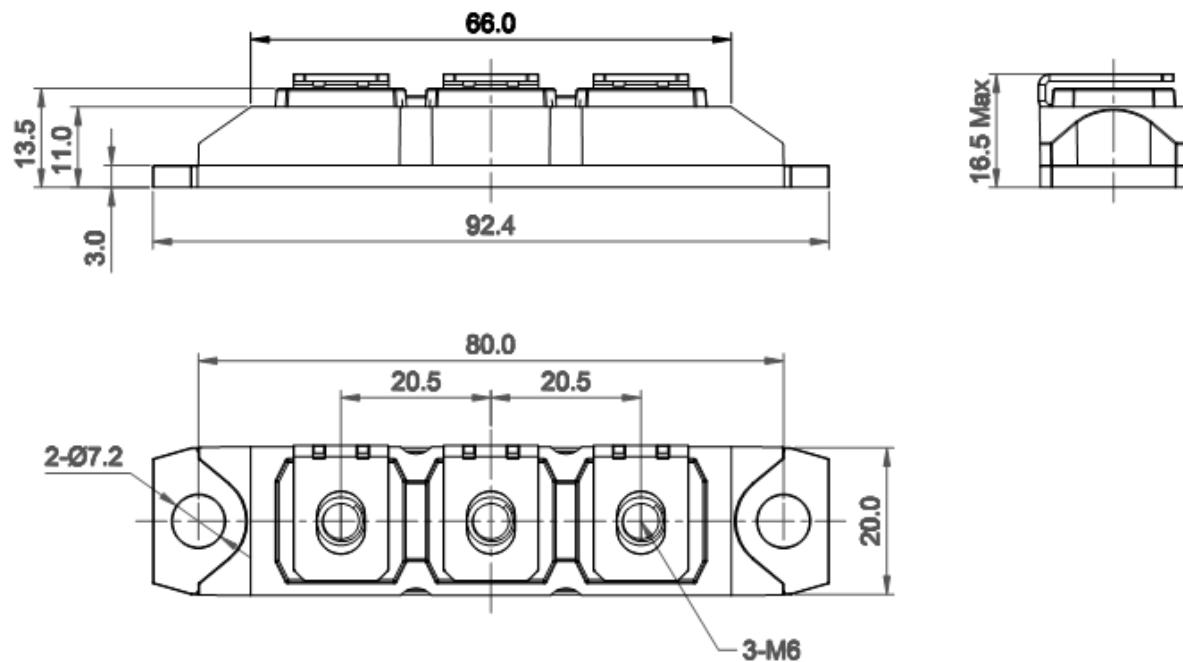
I =Insulated Package

M =Molding

Voltage rating (12 = 1200V)

Package Outline Information**M2 Package**

Dimensions in mm

M3 Package

Dimensions in mm